



OKAYA Electric America, Inc.

### SPECIFICATIONS

**DRAWING CODE**

\_\_\_\_\_

**SAMPLE CODE**

:

\_\_\_\_\_

(This Code will be changed while mass production)

**MASS PRODUCTION CODE** :

\_\_\_\_\_

**Customer Approved**

**Date:**

Sales Sign	QC Confirmed	Checked By	Designer

Approval For Specifications Only.

\* This specification is subject to change without notice.

Approval For Specifications and Sample.

**OKAYA Electric America, Inc.**  
52 Marks Road Suite 1 Valparaiso, IN 46383  
Phone: 800-852-0122 Fax: 219-477-4856



## History of Version

Date	Ver.	Description	Page	Design by
2007/03/02	0	New Drawing	-	Peter

Total : 8 Page



## Contents

### 1. SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 DC Electrical Characteristics

### 2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description

Appendix : LCM Drawing



## 1. SPECIFICATIONS

### 1.1 Features

Item	Standard Value
Display Type	800 X 3(RGB) X 480 Dots
LCD Type	Active matrix TFT , Transmissive type
Screen size(inch)	7-inch diagonal
Viewing Direction	6 O'clock
Color configuration	R.G.B. vertical stripe
Backlight Type	LED B/L
Interface	Digital parallel data input
ROHS	THIS PRODUCT CONFORMS THE ROHS

### 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	165.0 (L) * 104.0 (w) * 5.0(H)(Max)	mm
Viewing Area	155.3(W) x 94.4(H)	mm
Active Area	152.4(W) x 91.44(H)	mm

### 1.3 DC Electrical Characteristics

ITEM	SYMBOL	MIN.	MAX.	UNI
Power Supply Voltage	$V_{cc}$	-0.5	5.0	V
Operation Temperature	Top	-30	85	°C
Storage Temperature	Tstg	-40	90	°C

(1)TFT LCD

ITE	SYMBOL	MIN	TYP	MAX	UNIT
Power Supply Voltage For LCD	VCC	3.0	3.3	3.6	V
Power Supply Voltage For LED	VDD	4.5	5.0	5.5	V
Logic Input Voltage	VIH	$VCC \cdot 0.7$	-	VCC	V
	VIL	0	-	$VCC \cdot 0.3$	V

#### Backlight Characteristics

ITEM	SYMBOL	Min	Typ	Max	UNIT	Note
LED POWER CURRENT	IDD	-	300	350	mA	-
Power Supply Voltage For LED	VDD	4.5	5.0	5.5	V	-

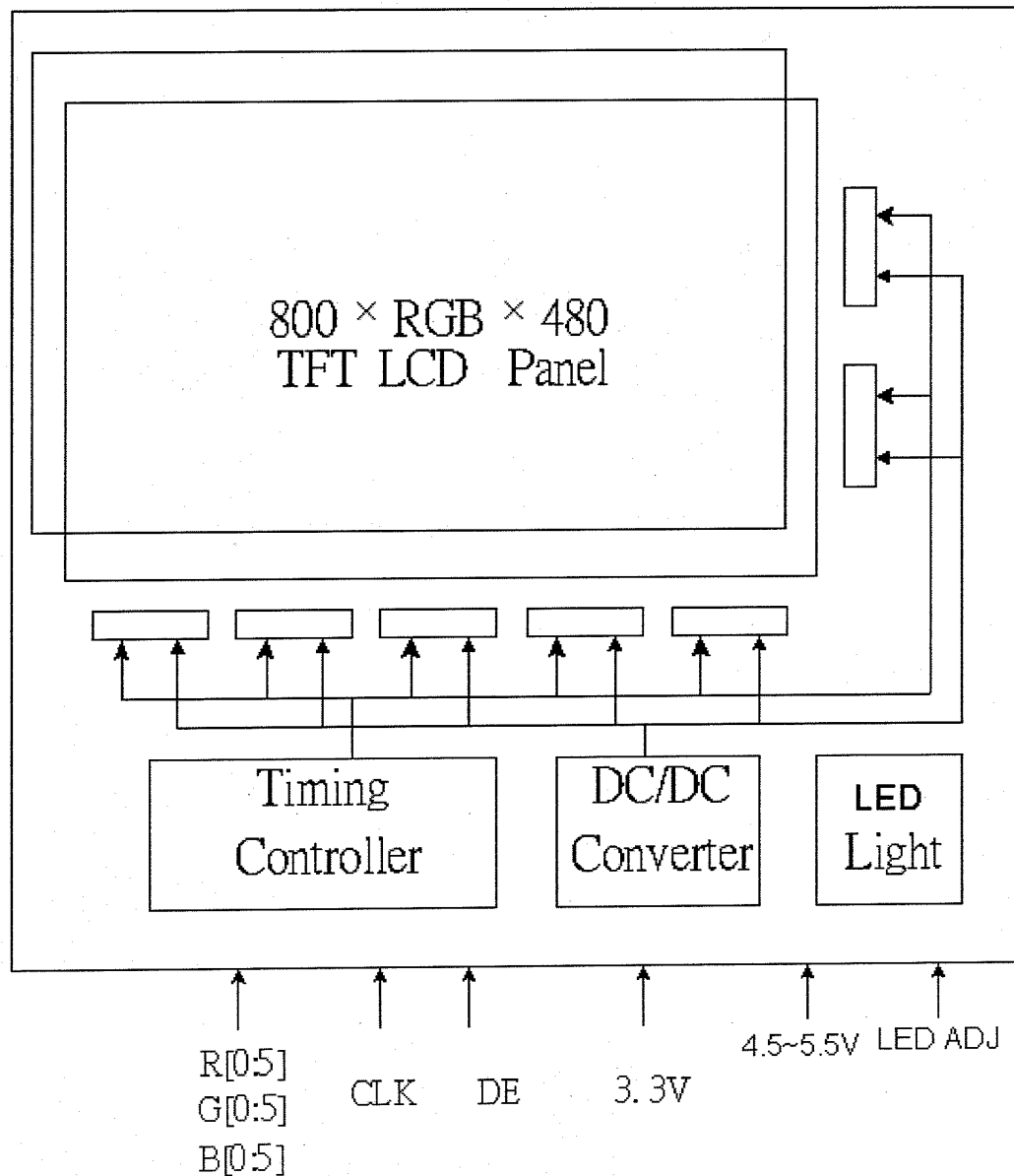
## 2. MODULE STRUCTURE

### 2.1 Counter Drawing

#### 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram



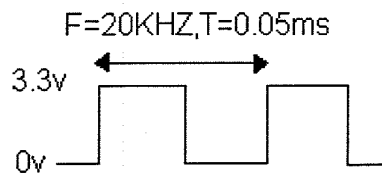
## 2.2 Interface Pin Description

Pin NO.	SYMBOL	DESCRIPTION
1	V <sub>SS</sub>	Power Ground
2	V <sub>SS</sub>	Power Ground
3	ADJ	Brightness control for LED B/L
4	V <sub>DD</sub>	Power Supply for LED Driver circuit
5	V <sub>DD</sub>	Power Supply for LED Driver circuit
6	V <sub>DD</sub>	Power Supply for LED Driver circuit
7	V <sub>CC</sub>	Power Supply for Digital Circuit
8	V <sub>CC</sub>	Power Supply for Digital Circuit
9	DE	Data Enable
10	V <sub>SS</sub>	Power Ground
11	V <sub>SS</sub>	Power Ground
12	V <sub>SS</sub>	Power Ground
13	B5	Blue Data 5 (MSB)
14	B4	Blue Data 4
15	B3	Blue Data 3
16	V <sub>SS</sub>	Power Ground
17	B2	Blue Data 2
18	B1	Blue Data 1
19	B0	Blue Data 0 (LSB)
20	V <sub>SS</sub>	Power Ground
21	G5	Green Data 5 (MSB)
22	G4	Green Data 4
23	G3	Green Data 3
24	V <sub>SS</sub>	Power Ground
25	G2	Green Data 2
26	G1	Green Data 1
27	G0	Green Data 0 (LSB)
28	V <sub>SS</sub>	Power Ground
29	R5	Red Data 5 (MSB)
30	R4	Red Data 4
31	R3	Red Data 3
32	V <sub>SS</sub>	Power Ground
33	R2	Red Data 2
34	R1	Red Data 1
35	R0	Red Data 0 (LSB)
36	V <sub>SS</sub>	Power Ground
37	V <sub>SS</sub>	Power Ground
38	DCLK	Data Clock, Latch Data at the Falling Edge.
39	V <sub>SS</sub>	Power Ground
40	V <sub>SS</sub>	Power Ground



# OKAYA

1. ADJ signal=0~3.3v, operation frequency: 20KHZ



2. GND Pin must ground contact, can not be floating.

